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lssue	DQDB	SMDS	X.25	Fram e Relay	ATM AAL
Connection oriented	Yes	No	Yes	Yes	Yes
Normal speed (Mbps)	45	45	.064	1.5	155
Switched	No	Yes	Yes	No	Yes
Fixed-size payload	Yes	No	No	No	No
Max payload	44	9188	128	1600	Variable
Permanent VCs	No	No	Yes	Yes	Yes
Multicasting	No	Yes	No	No	Yes

Compar	rison s	n with ervio	n otł ces	ner b	eare	er
Issue	DQDB	SMDS	X.25	Frame	ATM	ATM
				Relay		AAL
Connection oriented	Yes	No	Yes	Yes	Yes	Yes
Normal Speed (Mbps)	45	45	0.64	(1.5) 2	155	155
Switched	No	Yes	Yes	Yes	Yes	Yes
Fixed-size payload (octets)	Yes	No	No	No	Yes	No
Max. Payload size (octets)	44	9 K	128	1500	48	64 K
Multicasting	No	Yes	No	No	(Yes)	(IP)





























OSI la yer	ATM layer	ATM sublayer	Functionality
		CS	Providing the standard interface (convergence)
3/4	AAL	SAR	Segmentation and reassembly
2/3	ATM		Flow control Cell header generation/extraction Virtual circuit/path management Cell multiplexing/demultiplexing
2	Physical	тс	Cell rate decoupling Header checksum generation and verification Cell generation Packing/unpacking cells from the enclosing envelop Frame generation
1	I hysicat	PMD	Bit tim ing Physical network access





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		and			lato
SON Electrical	Ontical	Ontical	Gross	spe	llser
STS-1	0C-1		51.84	50.112	49.53
STS-3	OC-3	STM-1	155.52	150.336	148.60
STS-9	OC-9	STM-3	466.56	451.008	445.824
STS-12	OC-12	STM-4	622.08	601.344	594.432
STS-18	OC-18	STM-6	933.12	902.016	891.648
STS-24	OC-24	STM-8	1244.16	1202.688	1188.864
STS-36	OC-36	STM-12	1866.24	1804.032	1783.296
STS_48	OC-48	STM-16	2488.32	2405.376	2377.728



































		Lay	ver Functions			
OSI la yer	ATM layer	ATM sublayer	Functionality			
		CS	Providing the standard interface (convergence)			
3/4	AAL	AAL	AAL	SAR	Segmentation and reassembly	
2/3	ATM		Flow control Cell header genetation/extraction Virtual circuit/path managem ent Cell multiplexing/demultiplexing			
2	Physical	тс	Cell rate decoupling Header checksum generation and verification Cell generation Packing/unpacking cells from the enclosing envelope Frame generation			
1	1 1195 16 01 1	PMD	Bit tim ing Physical network access			







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IICAUCI		гнуз	icai L	ayei
Cell type	Octet 1	Octet 2	Octet 3	Octet 4
IDLE cells	00000000	00000000	00000000	00000001
Physical Layer OAM	00000000	00000000	00000000	00001001
Reserved for use by Physical Layer	PPP0000	00000000	00000000	0000PPP
· · · ·				
P : Bit is available for us	e by the PHY lav	er		





Payload type	Meaning
000	User data cell, no congestion, cell type 0
001	User data cell, no congestion, cell type 1
010	User data cell, congestion experienced, cell type 0
011	User data cell, congestion experienced, cell type 1
100	Maintenance information between adjacent switches
101	Maintenance information between source and destination switches
110	Resource Management cell (used for ABR congestion control)
111	Reserved for future function























Advantages of the VP/VC concept

- Simplified network architecture. VC switching for individual logical connections and VP switching for a group of logical connections.
- Increased network performance and reliability.
- Reduced processing for new VCCs in an already established VPC.
- Enhanced network services (closed user groups).